

adding the correction signal to the first reference signal to produce an output signal.

**13.** The method of claim **12**, further comprising:  
generating a third signal that varies with temperature along a third constant or continuously variable slope; rectifying the third signal to produce a second rectified signal; and  
adding the second rectified signal to the first rectified signal and the second signal to produce the correction signal.

**14.** The method of claim **13**, wherein the first rectified signal varies with temperature for a first range of temperature, the first range of temperature corresponding to a first region where a temperature of a temperature dependent correction circuit is less than a first temperature threshold, and the second rectified signal varies with temperature for a second range of temperature, the second range of temperature corresponding to a second region where a temperature of the temperature dependent correction circuit exceeds a second temperature threshold.

**15.** The method of claim **14**, wherein the first rectified signal is approximately zero during a third range of temperature, the third range of temperature corresponding to a third region where the temperature of the temperature dependent correction circuit exceeds the first temperature threshold and the second rectified signal is approximately zero during a fourth range of temperature, the fourth range of temperature corresponding to a time where the temperature of the temperature dependent correction circuit is less than the second temperature threshold.

**16.** The method of claim **12**, wherein the rectifying the first signal comprises passing only a positive signal com-

ponent of the first signal or only a negative signal component of the first signal through a rectifying circuit.

**17.** The method of claim **16**, wherein the rectifying circuit comprises a diode.

**18.** A reference, comprising:

generation logic configured to generate a first reference signal that varies with temperature; and

adding logic configured to add the first reference signal to a correction signal received from a rectifying circuit to produce an output signal, the correction signal comprising a rectified signal added to a first signal;

wherein the rectified signal comprises a first component that varies with temperature along a first constant or continuously variable slope and a second component that is approximately zero; and

wherein the first reference signal varies with temperature along a second constant or continuously variable slope.

**19.** The reference of claim **18**, wherein the first component of the rectified signal comprises only a negative signal or only a positive signal.

**20.** The reference of claim **19**, wherein the rectified signal varies with temperature for a first range of temperature corresponding to a first region where a temperature of the voltage reference is less than a temperature threshold and the rectified signal is approximately zero for a second range of temperature corresponding to a second region where the temperature of the voltage reference exceeds the temperature threshold.

**21.** The reference of claim **20**, wherein the rectifying circuit comprises a diode.

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